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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Theodore W. Houston, et al.

Docket No: TI-32107

Serial No: 10/039,124

Conf. No: 1057

Examiner: Johannes P. Mondt

Art Unit: 2826

Filed: 12/31/2001

For: METHOD AND APPARATUS FOR REDUCING LEAKAGE CURRENT IN AN SRAM ARRAY

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 9-4-03.


Ann Trent

Dear Sir:

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed June 4, 2003, finally rejecting Claims 1-35 and 37.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection

☐ was granted on _____.
☐ is requested for ____ month.

2. ☒ A timely response to the final rejection has been filed.

3. ☒ Fee \$320.00:

☐ Not required (Fee paid in prior appeal)

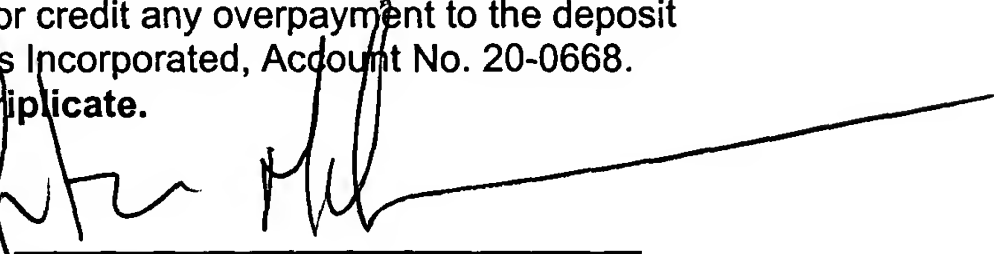
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

This form is submitted in triplicate.

09/09/2003 HGBREM1 00000043 200668 10039124

01 FC:1401 320.00 DA

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